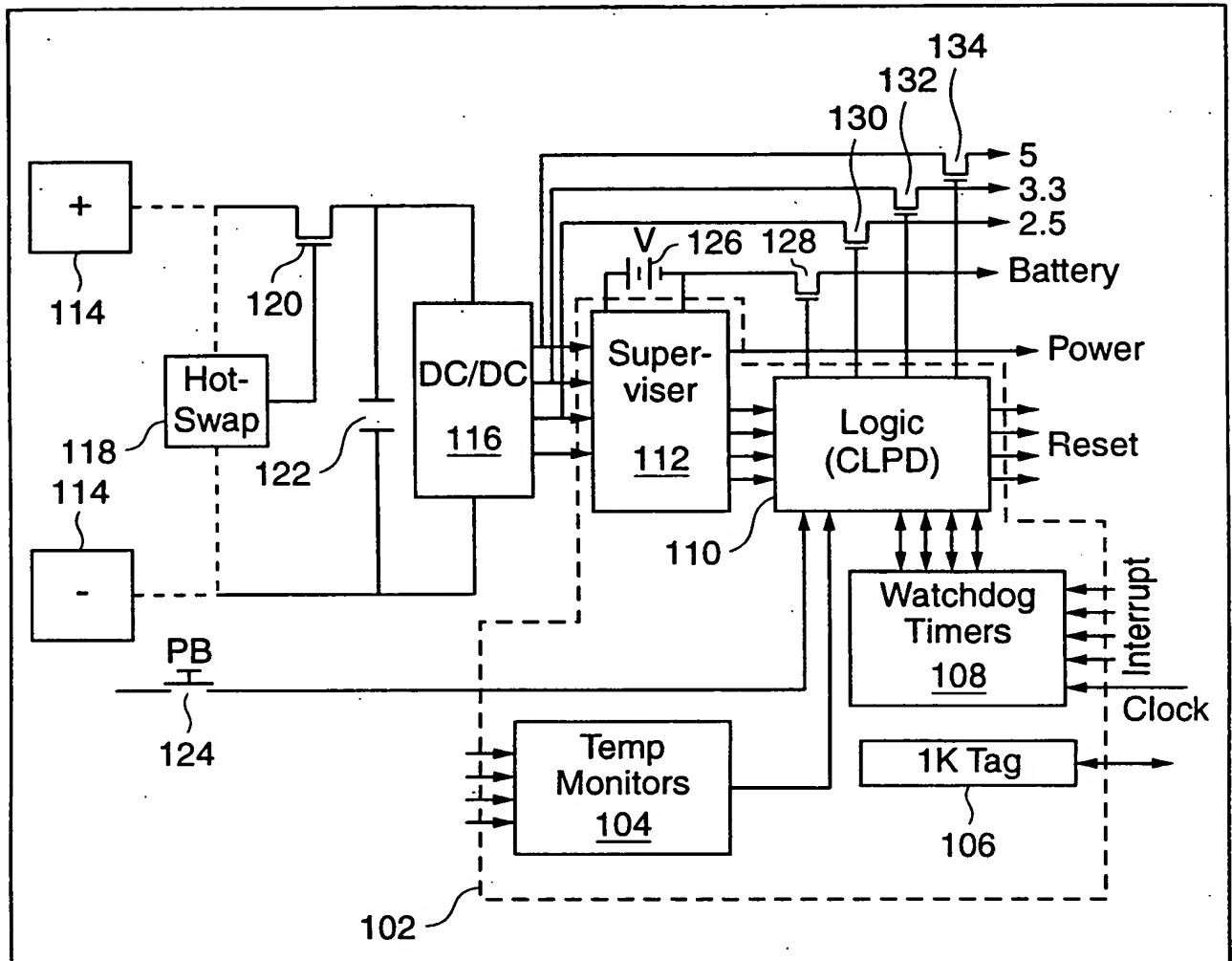


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100

FIG. 1

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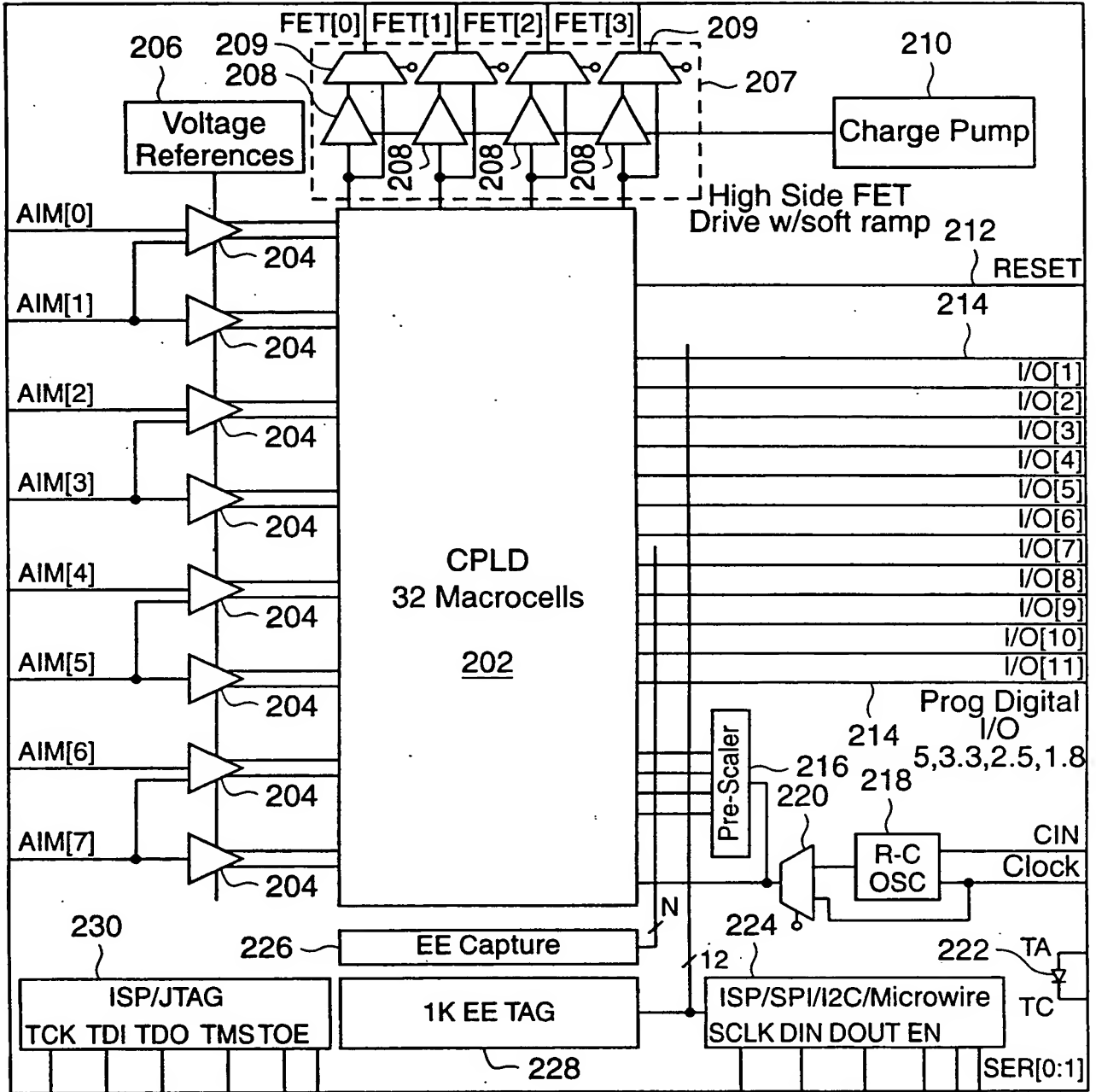


FIG. 2

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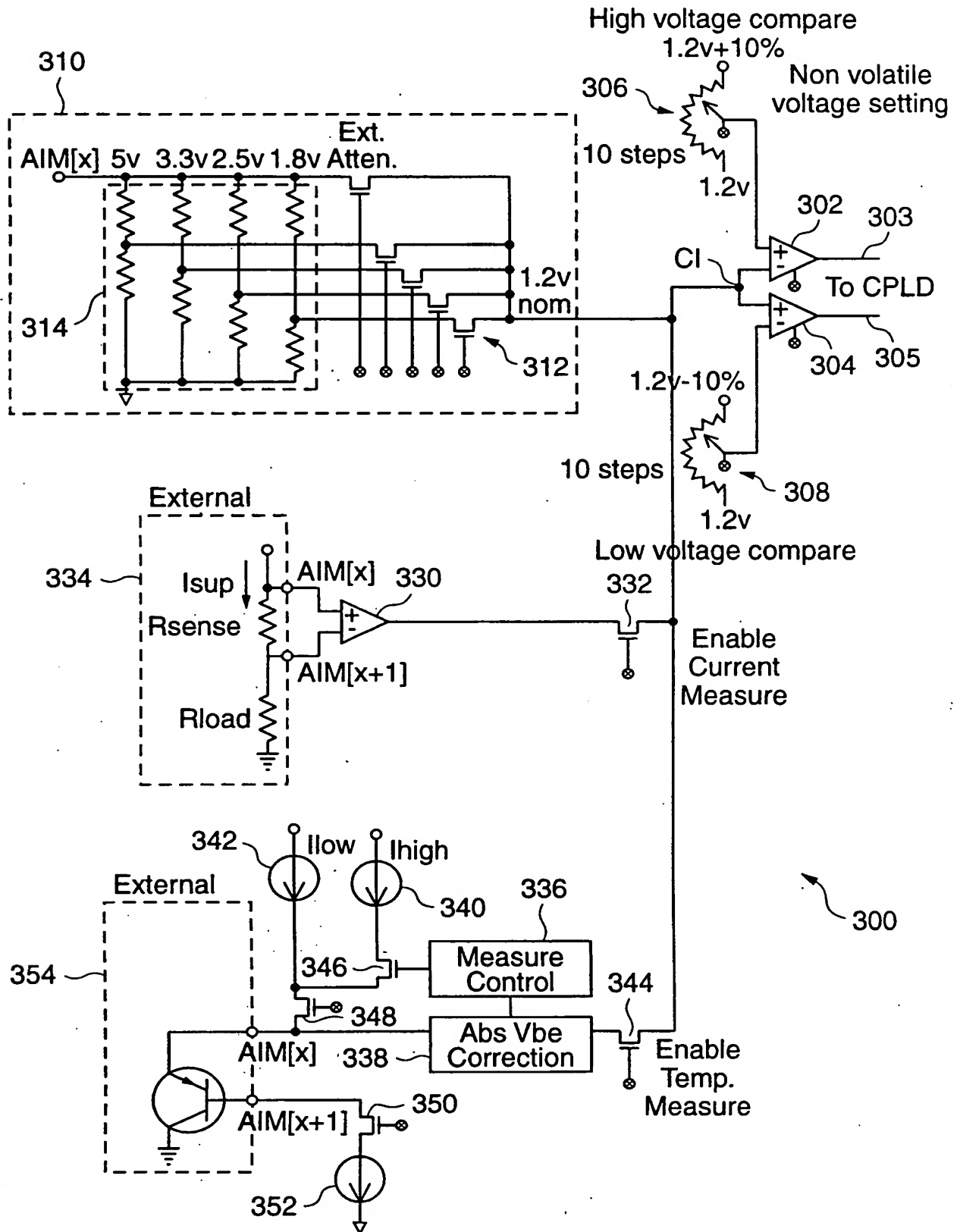


FIG. 3

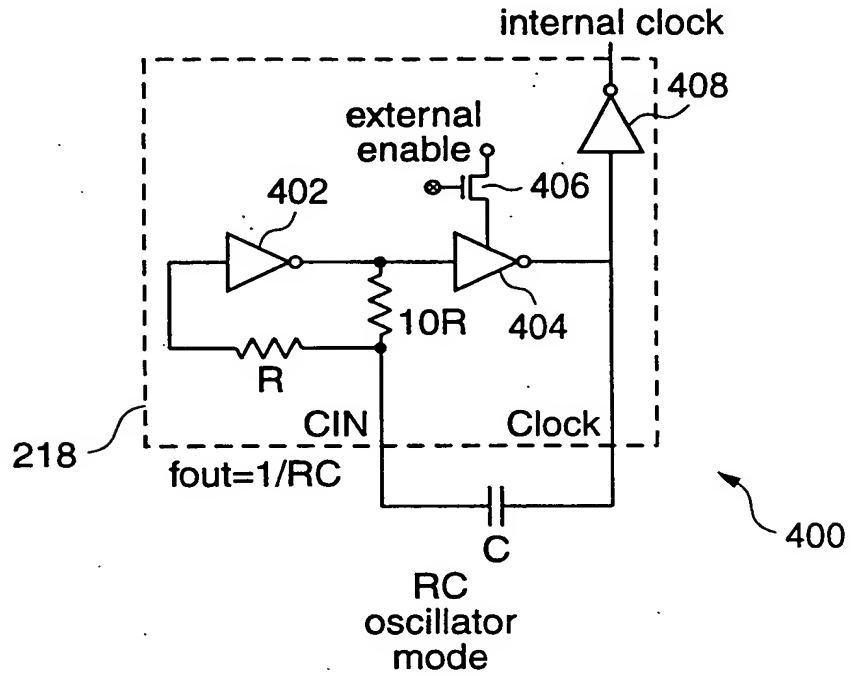


FIG. 4

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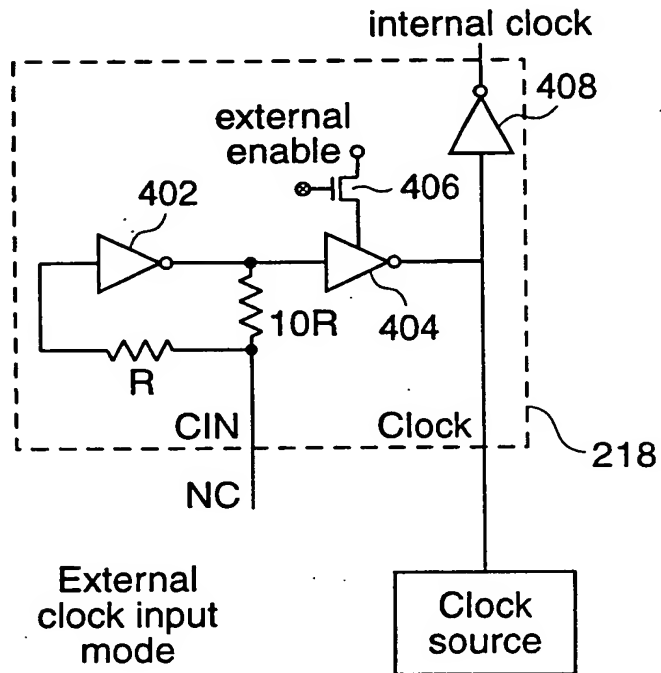


FIG. 5

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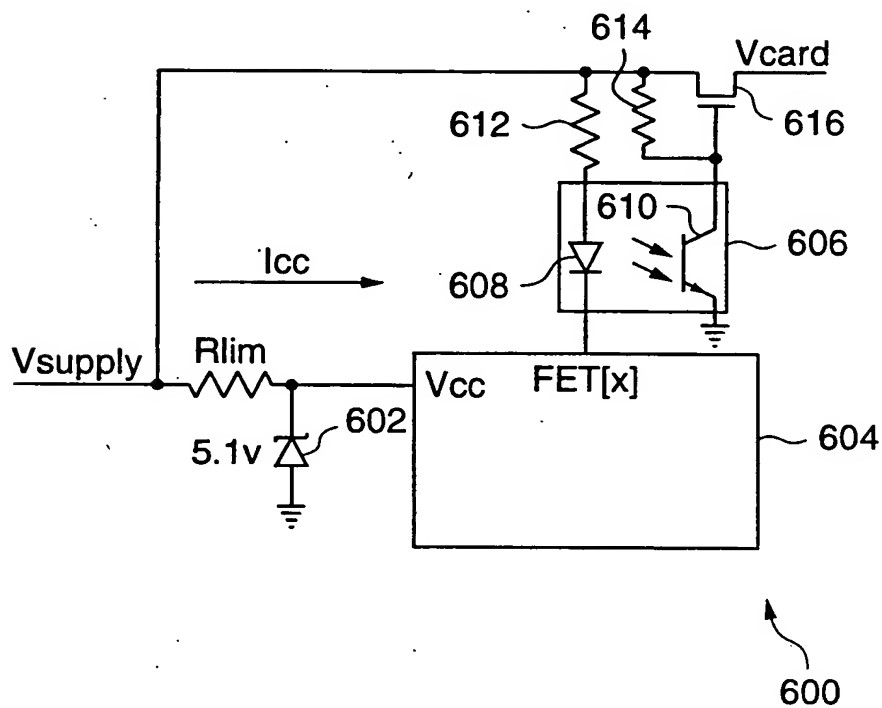


FIG. 6

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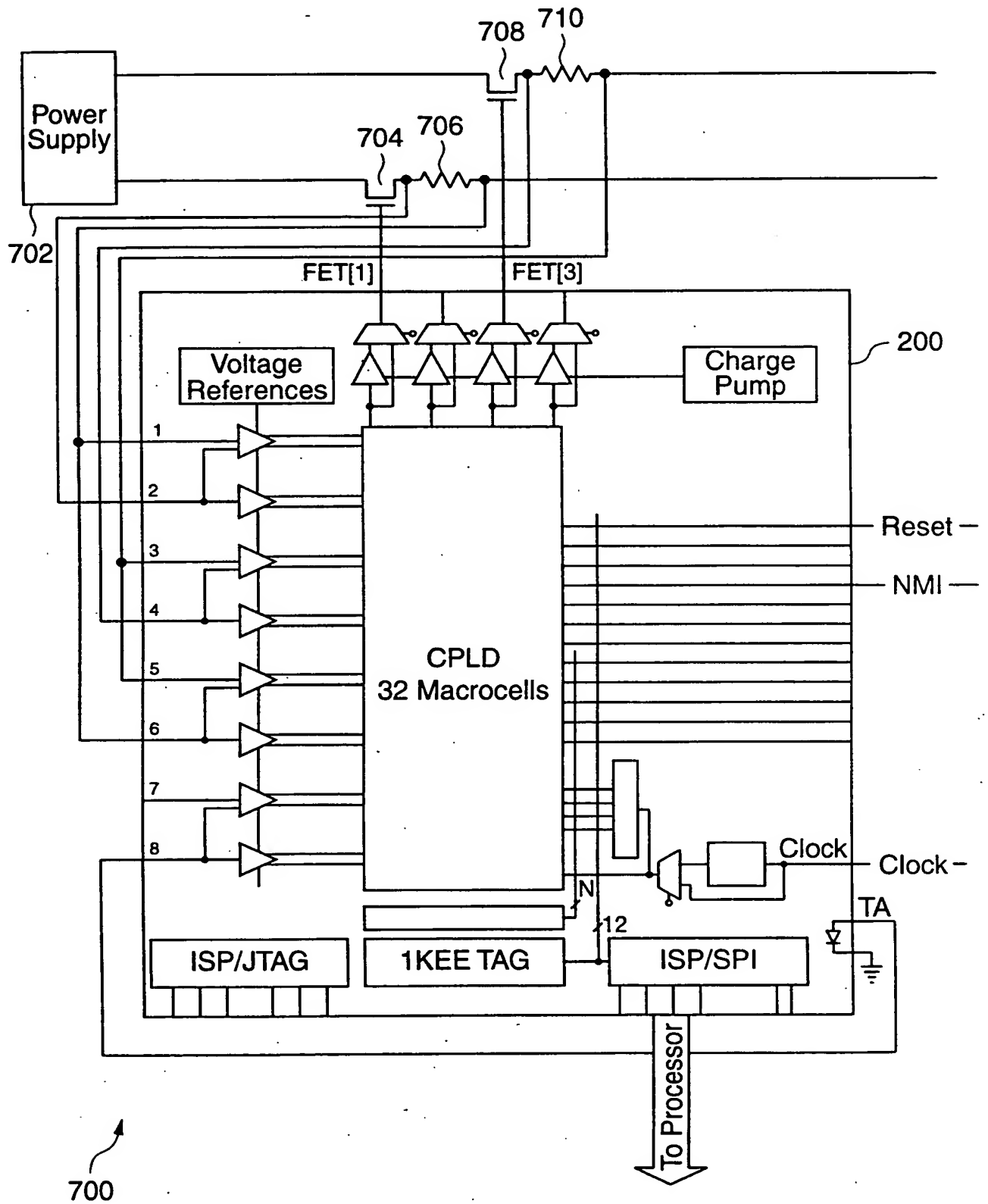
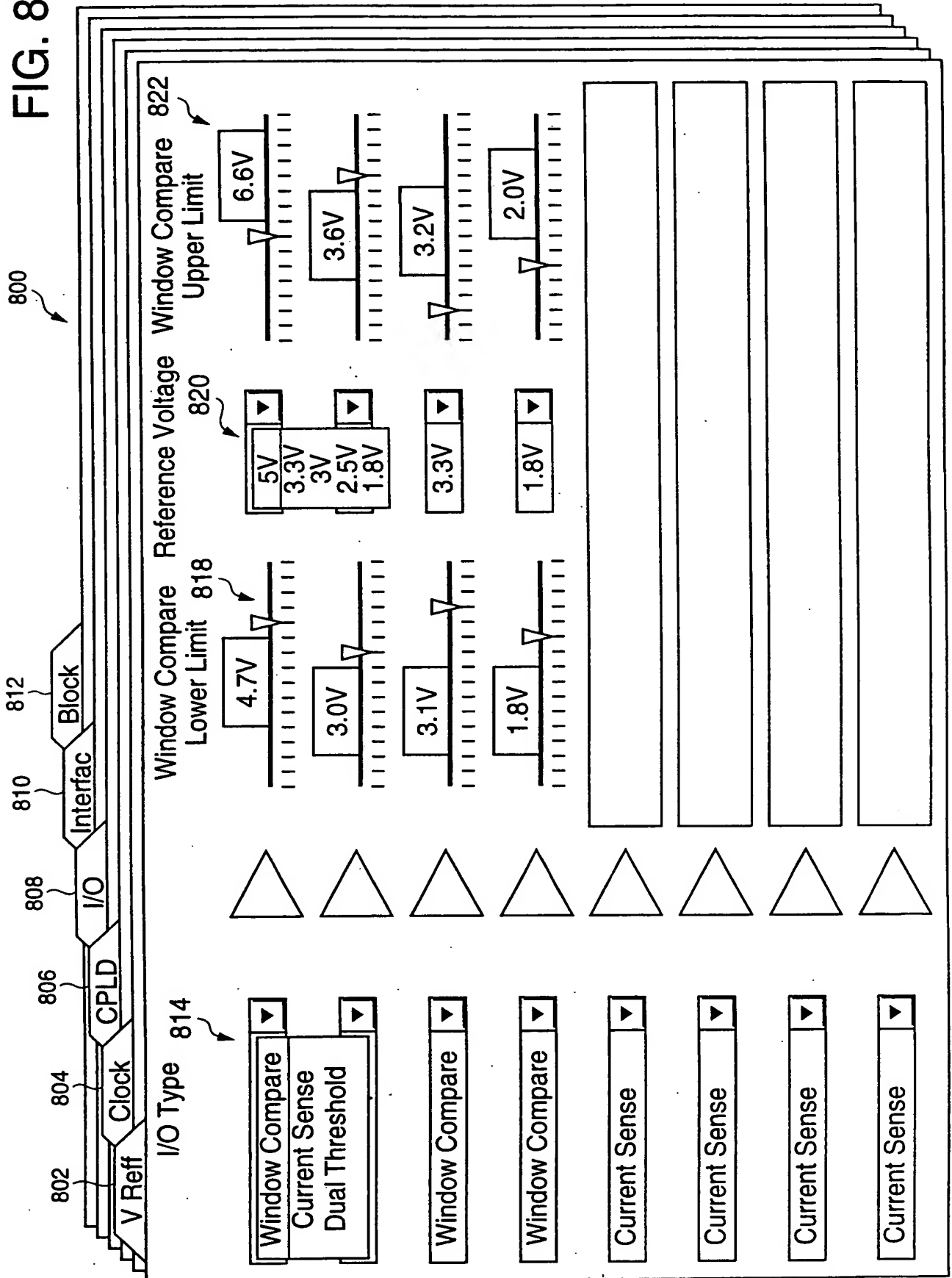


FIG. 7

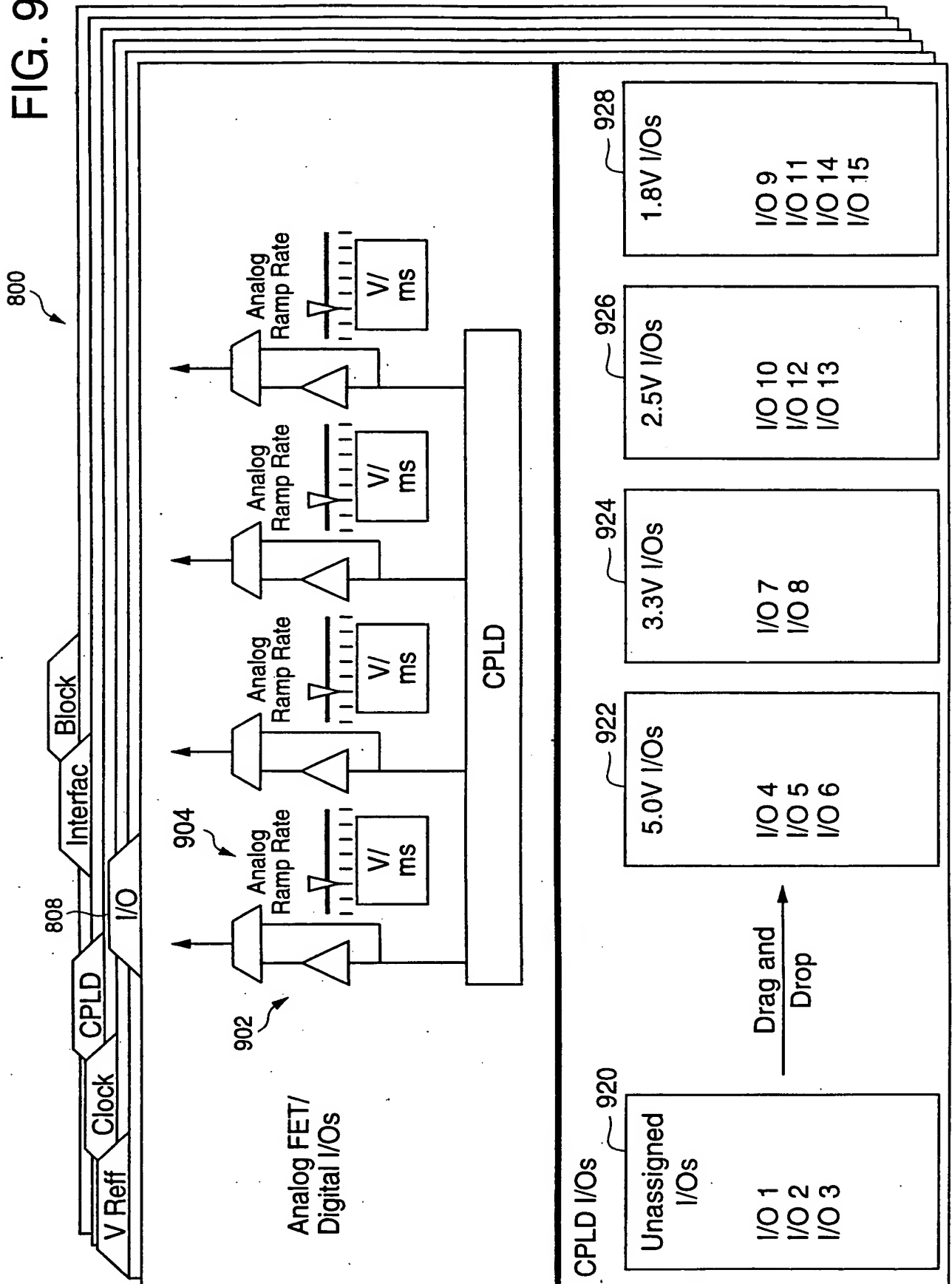
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FIG. 8



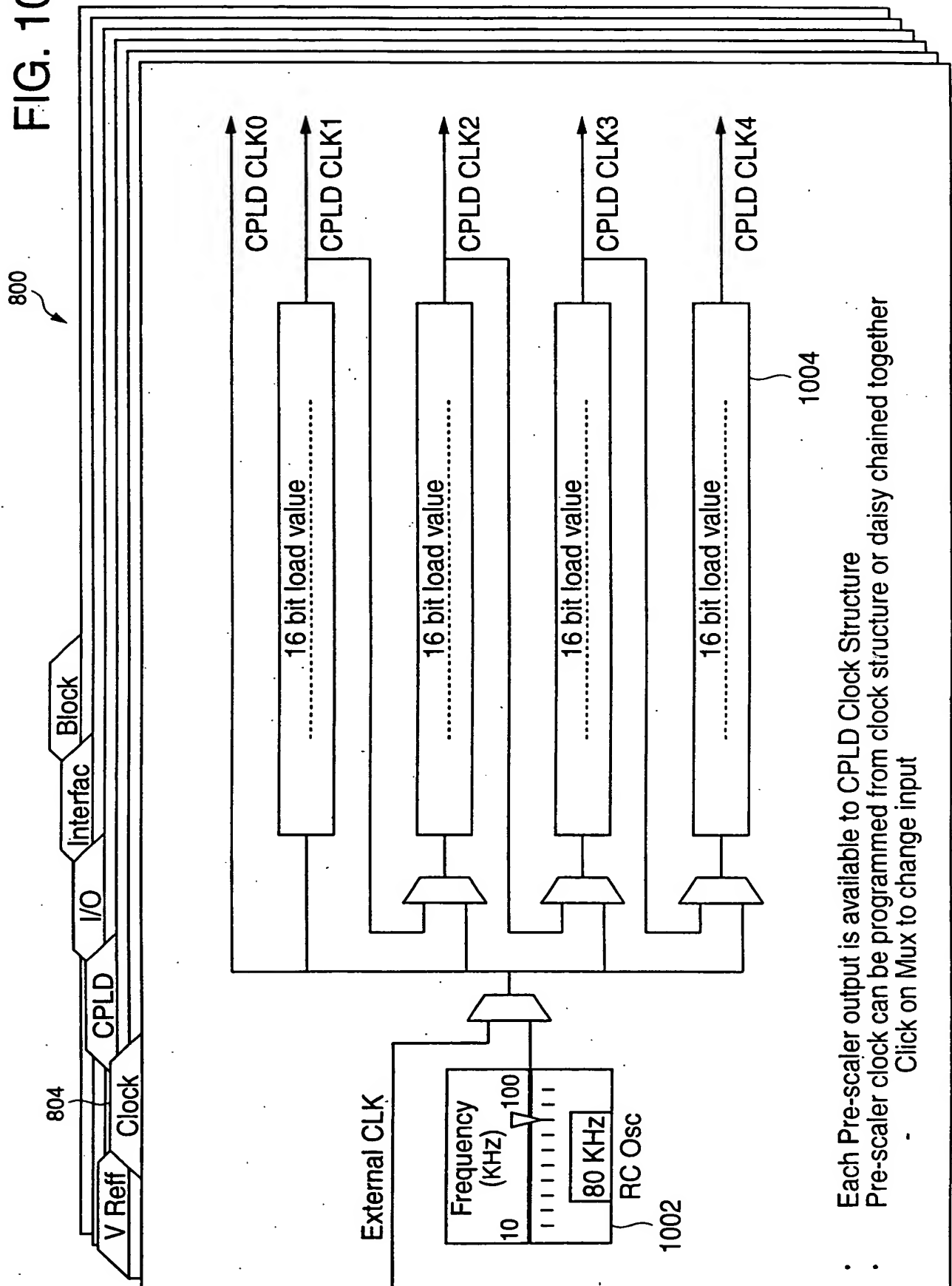
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FIG. 9



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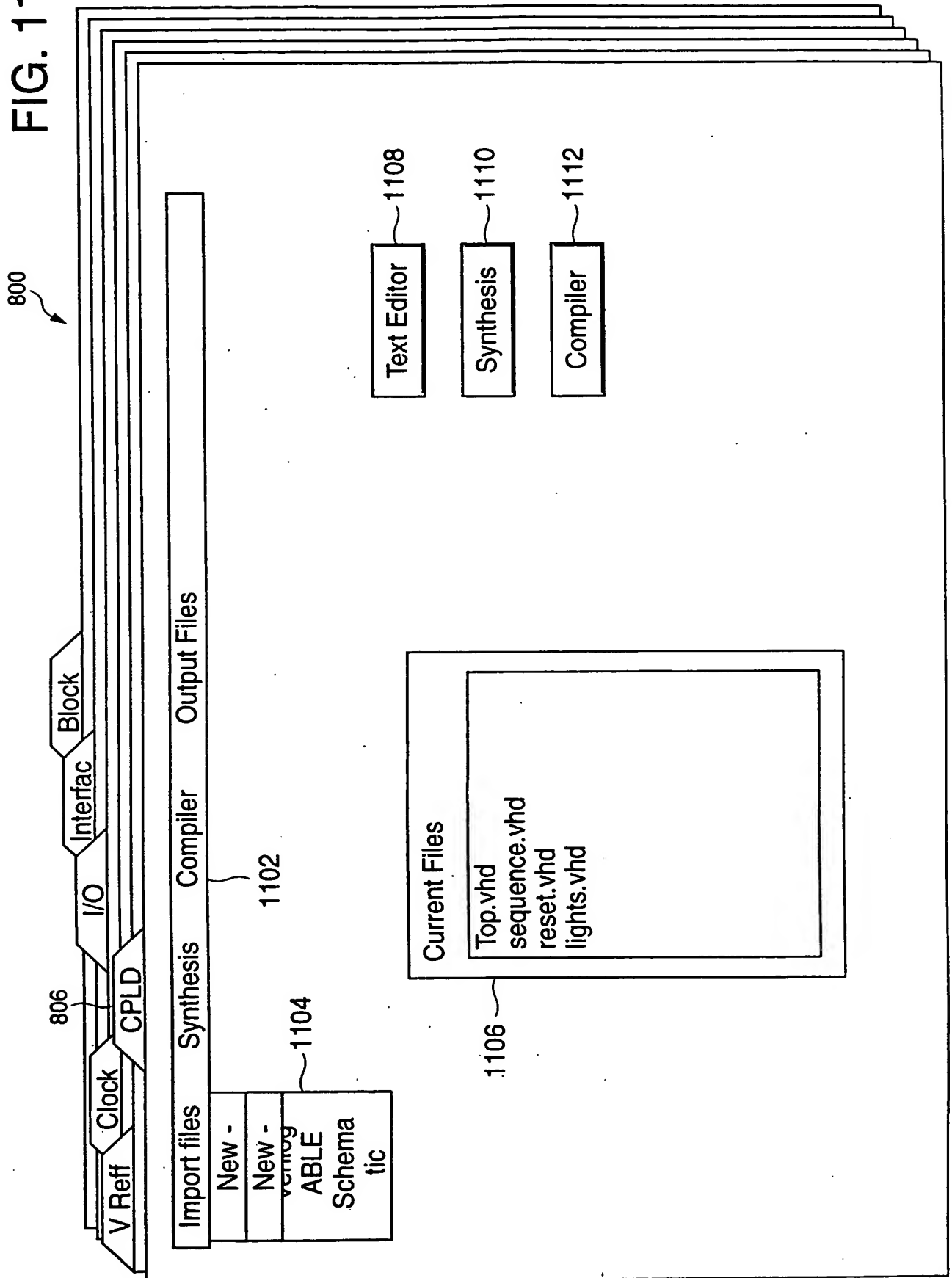
FIG. 10



- Each Pre-scaler output is available to CPLD Clock Structure
- Pre-scaler clock can be programmed from clock structure or daisy chained together
- Click on Mux to change input

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FIG. 11



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FIG. 12

800

